

REMARKS

Reconsideration is respectfully requested.

Objection to the Drawings

Applicant notes the Examiner's objections and will correct the discrepancies in the Formal drawings.

Rejections Under 35 U.S.C. §112, Second Paragraph

Claims 1-23 are rejected under 35 U.S.C. §112, second paragraph for indefiniteness. The Examiner has cited specific language in Claims 1, 14, 16 and 21-23 as indefinite. Claims 1, 14, 16, 21 and 23 are amended to be more definite, and amended Claim 21 provides antecedent basis for "voltage requirements" recited in Claim 22.

Rejections Under 35 U.S.C. §102

Claim 21 is rejected under 35 U.S.C. §102(b) as being anticipated by Smith. Applicant respectfully traverses this rejection.

Smith discloses a power manager which

... continually monitors various circuit functions such that devices not in use have their power sources or clock signals disconnected in order to deactivate devices to conserve battery power (column 3, lines 33-36). (emphasis added)

Thus, Smith merely teaches disconnecting power sources from devices to conserve power. Further, in regard to volatile RAM, Smith states:

However, if RAM 14 is comprised of volatile memory then the transistor switch applying Vcc power to RAM 14 is kept closed so that Vcc is still applied to RAM 14 keeping it active in order to retain the stored information. It is to be noted that non-volatile memory is preferred so that Vcc need not be applied to RAM 14 in the sleep mode (column 7, line 66 to column 8, line 4). (emphasis added)

In contrast, Claim 21, as amended, recites in pertinent part:

logic control means for causing said power control means to supply to said memory integrated circuit a first voltage during said first operation period, a second voltage different from said first voltage during said second operation period, and a third voltage different from said first and second voltages during said third operation period. (emphasis added)

Smith in no way teaches or suggests a logic control means according to Claim 21 because Smith's power manager merely disconnects the power to a device. Further, Smith specifically teaches supplying Vcc to volatile RAM even during sleep mode. Smith's power manager does not supply three different voltages to a memory integrated circuit as recited in Claim 21. Thus, Claim 21 is not anticipated by Smith.


Claim 22 is dependent from Claim 21 and is distinguished from Smith for at least the same reasons that Claim 21 is distinguished.

#### Conclusion

Claims 1-23 are pending. Claims 1, 14, 16, 21 and 23 are amended. No new matter is added. For the reasons presented above, Applicant believes that Claims 1-23 are in condition for allowance. Accordingly, a Notice of Allowance is respectfully requested.

If the Examiner's next action is other than allowance of Claims 1-23, the Examiner is respectfully requested to call Applicant's attorney at (408) 283-1222.

Respectfully submitted,



Alan H. MacPherson  
Attorney for Applicants  
Reg. No. 24,423

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C., 20231, on November 3, 1994.

Nov 3, 1994

Date of Signature



Attorney for Applicant(s)